

FORM PTO-1390 (Modified) (REV 11-98)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTORNEY'S DOCKET NUMBER <b>205235US0PCT</b>	
<b>TRANSMITTAL LETTER TO THE UNITED STATES</b> <b>DESIGNATED/ELECTED OFFICE (DO/EO/US)</b> <b>CONCERNING A FILING UNDER 35 U.S.C. 371</b>				U.S. APPLICATION NO. (IF KNOWN, SEE 37 CFR <b>09/831887</b>	
INTERNATIONAL APPLICATION NO. <b>PCT/JP00/02909</b>		INTERNATIONAL FILING DATE <b>02 MAY 2000</b>		PRIORITY DATE CLAIMED <b>09 AUGUST 1999 (EARLIEST)</b>	
TITLE OF INVENTION <b>CERAMIC HEATER</b>					
APPLICANT(S) FOR DO/EO/US <b>ITO Yasutaka et al.</b>					
Applicant herewith submits to the United States Designated/Elected Office (DO/EO/US) the following items and other information:					
<ol style="list-style-type: none"> <li>1. <input checked="" type="checkbox"/> This is a <b>FIRST</b> submission of items concerning a filing under 35 U.S.C. 371.</li> <li>2. <input checked="" type="checkbox"/> This is a <b>SECOND</b> or <b>SUBSEQUENT</b> submission of items concerning a filing under 35 U.S.C. 371.</li> <li>3. <input checked="" type="checkbox"/> This is an express request to begin national examination procedures (35 U.S.C. 371(f)) at any time rather than delay examination until the expiration of the applicable time limit set in 35 U.S.C. 371(b) and PCT Articles 22 and 39(1).</li> <li>4. <input checked="" type="checkbox"/> A proper Demand for International Preliminary Examination was made by the 19th month from the earliest claimed priority date.</li> <li>5. <input checked="" type="checkbox"/> A copy of the International Application as filed (35 U.S.C. 371 (c) (2))           <ol style="list-style-type: none"> <li>a. <input type="checkbox"/> is transmitted herewith (required only if not transmitted by the International Bureau).</li> <li>b. <input checked="" type="checkbox"/> has been transmitted by the International Bureau.</li> <li>c. <input type="checkbox"/> is not required, as the application was filed in the United States Receiving Office (RO/US).</li> </ol> </li> <li>6. <input checked="" type="checkbox"/> A translation of the International Application into English (35 U.S.C. 371(c)(2)).</li> <li>7. <input checked="" type="checkbox"/> A copy of the International Search Report (PCT/ISA/210).</li> <li>8. <input checked="" type="checkbox"/> Amendments to the claims of the International Application under PCT Article 19 (35 U.S.C. 371 (c)(3))           <ol style="list-style-type: none"> <li>a. <input type="checkbox"/> are transmitted herewith (required only if not transmitted by the International Bureau).</li> <li>b. <input type="checkbox"/> have been transmitted by the International Bureau.</li> <li>c. <input type="checkbox"/> have not been made; however, the time limit for making such amendments has NOT expired.</li> <li>d. <input checked="" type="checkbox"/> have not been made and will not be made.</li> </ol> </li> <li>9. <input type="checkbox"/> A translation of the amendments to the claims under PCT Article 19 (35 U.S.C. 371(c)(3)).</li> <li>10. <input checked="" type="checkbox"/> An oath or declaration of the inventor(s) (35 U.S.C. 371 (c)(4)).</li> <li>11. <input type="checkbox"/> A copy of the International Preliminary Examination Report (PCT/IPEA/409).</li> <li>12. <input type="checkbox"/> A translation of the annexes to the International Preliminary Examination Report under PCT Article 36 (35 U.S.C. 371 (c)(5)).</li> </ol>					
<b>Items 13 to 20 below concern document(s) or information included:</b>					
<ol style="list-style-type: none"> <li>13. <input type="checkbox"/> An Information Disclosure Statement under 37 CFR 1.97 and 1.98.</li> <li>14. <input type="checkbox"/> An assignment document for recording. A separate cover sheet in compliance with 37 CFR 3.28 and 3.31 is included.</li> <li>15. <input checked="" type="checkbox"/> A <b>FIRST</b> preliminary amendment.</li> <li>16. <input type="checkbox"/> A <b>SECOND</b> or <b>SUBSEQUENT</b> preliminary amendment.</li> <li>17. <input type="checkbox"/> A substitute specification.</li> <li>18. <input type="checkbox"/> A change of power of attorney and/or address letter.</li> <li>19. <input type="checkbox"/> Certificate of Mailing by Express Mail</li> <li>20. <input checked="" type="checkbox"/> Other items or information:</li> </ol>					
<div style="border: 1px solid black; padding: 5px;"> <b>Request for Consideration of Documents Cited in International Search Report</b>  <b>Notice of Priority</b>  <b>Drawings (4 sheets)</b> </div>					

U.S. APPLICATION NO. (IF KNOWN, SEE 37 CFR <b>09/831887</b> )		INTERNATIONAL APPLICATION NO. <b>PCT/JP00/02909</b>		ATTORNEY'S DOCKET NUMBER <b>205235US0PCT</b>	
--	--	--	--	---	--

21. The following fees are submitted: <b>BASIC NATIONAL FEE ( 37 CFR 1.492 (a) (1) - (5) ) :</b> <ul style="list-style-type: none"> <li><input type="checkbox"/> Neither international preliminary examination fee (37 CFR 1.482) nor international search fee (37 CFR 1.445(a)(2)) paid to USPTO and International Search Report not prepared by the EPO or JPO ..... <b>\$1,000.00</b></li> <li><input checked="" type="checkbox"/> International preliminary examination fee (37 CFR 1.482) not paid to USPTO but International Search Report prepared by the EPO or JPO ..... <b>\$860.00</b></li> <li><input type="checkbox"/> International preliminary examination fee (37 CFR 1.482) not paid to USPTO but international search fee (37 CFR 1.445(a)(2)) paid to USPTO ..... <b>\$710.00</b></li> <li><input type="checkbox"/> International preliminary examination fee paid to USPTO (37 CFR 1.482) but all claims did not satisfy provisions of PCT Article 33(1)-(4) ..... <b>\$690.00</b></li> <li><input type="checkbox"/> International preliminary examination fee paid to USPTO (37 CFR 1.482) and all claims satisfied provisions of PCT Article 33(1)-(4) ..... <b>\$100.00</b></li> </ul>				<b>CALCULATIONS PTO USE ONLY</b>	
<b>ENTER APPROPRIATE BASIC FEE AMOUNT =</b>				<b>\$860.00</b>	
Surcharge of <b>\$130.00</b> for furnishing the oath or declaration later than <input type="checkbox"/> 20 <input type="checkbox"/> 30 months from the earliest claimed priority date (37 CFR 1.492 (e)).				<b>\$0.00</b>	
CLAIMS	NUMBER FILED	NUMBER EXTRA	RATE		
Total claims	12 - 20 =	0	x \$18.00	\$0.00	
Independent claims	4 - 3 =	1	x \$80.00	\$80.00	
Multiple Dependent Claims (check if applicable). <input type="checkbox"/>				\$0.00	
<b>TOTAL OF ABOVE CALCULATIONS =</b>				<b>\$940.00</b>	
Reduction of 1/2 for filing by small entity, if applicable. Verified Small Entity Statement must also be filed (Note 37 CFR 1.9, 1.27, 1.28) (check if applicable). <input type="checkbox"/>				\$0.00	
<b>SUBTOTAL =</b>				<b>\$940.00</b>	
Processing fee of <b>\$130.00</b> for furnishing the English translation later than <input type="checkbox"/> 20 <input type="checkbox"/> 30 months from the earliest claimed priority date (37 CFR 1.492 (f)).				\$0.00	
<b>TOTAL NATIONAL FEE =</b>				<b>\$940.00</b>	
Fee for recording the enclosed assignment (37 CFR 1.21(h)). The assignment must be accompanied by an appropriate cover sheet (37 CFR 3.28, 3.31) (check if applicable). <input type="checkbox"/>				\$0.00	
<b>TOTAL FEES ENCLOSED =</b>				<b>\$940.00</b>	
				Amount to be: refunded	\$
				charged	\$


  

☒ A check in the amount of **\$940.00** to cover the above fees is enclosed.  
  
☐ Please charge my Deposit Account No. \_\_\_\_\_ in the amount of \_\_\_\_\_ to cover the above fees.  
 A duplicate copy of this sheet is enclosed.  
  
☒ The Commissioner is hereby authorized to charge any fees which may be required, or credit any overpayment to Deposit Account No. **15-0030** A duplicate copy of this sheet is enclosed.

**NOTE: Where an appropriate time limit under 37 CFR 1.494 or 1.495 has not been met, a petition to revive (37 CFR 1.137(a) or (b)) must be filed and granted to restore the application to pending status.**

SEND ALL CORRESPONDENCE TO:



**22850**

*Norman F. Oblon*

SIGNATURE

**Norman F. Oblon**

NAME

**24,618**

REGISTRATION NUMBER

*May 23 2001*

DATE

205235US0PCT

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF: :  
YASUTAKA ITO ET AL : ATTN: APPLICATION DIVISION  
SERIAL NO: NEW US PCT APPLN. :  
(BASED ON PCT/JP00/02909)  
FILED: HEREWITH :  
FOR: CERAMIC HEATER

PRELIMINARY AMENDMENT

ASSISTANT COMMISSIONER FOR PATENTS  
WASHINGTON, D.C. 20231

SIR:

Prior to examination on the merits, please amend the above-identified application as follows.

IN THE CLAIMS

Please amend the claims as follows:

- 3. (Amended) The ceramic heater according to Claim 1  
wherein said ceramic substrate is at least one member selected from the group  
consisting of a nitride ceramic, a carbide ceramic and an oxide ceramic.
6. (Amended) The ceramic heater according to Claim 4  
wherein said nitride ceramic board has the form of a disk having a diameter of more  
than 150 mm.

7. (Amended) The ceramic heater according to Claim 4  
wherein the content of at least one element selected from the group consisting of Y,  
Li, Rb and Ca is not less than 0.1 weight %.

8. (Amended) The ceramic heater according to Claim 4  
wherein the content of at least one element selected from the group consisting of Na  
and B is not less than 0.05 ppm.--

Please add the following claims:

--9. (New) The ceramic heater according to Claim 2,  
wherein said ceramic substrate is at least one member selected from the group  
consisting of a nitride ceramic, a carbide ceramic and an oxide ceramic.

10. (New) The ceramic heater according to Claim 5  
wherein said nitride ceramic board has the form of a disk having a diameter of more  
than 150 mm.

11. (New) The ceramic heater according to Claim 5  
wherein the content of at least one element selected from the group consisting of Y,  
Li, Rb and Ca is not less than 0.1 weight %.

12. (New) The ceramic heater according to Claim 5  
wherein the content of at least one element selected from the group consisting of Na  
and B is not less than 0.05 ppm.

REMARKS

Claims 1-12 are active in the present application. The claims are amended to remove multiple dependencies. Support for Claims 9-12 is found in Claims 3, 6-8 and the specification as originally filed. No new matter is added. An action on the merits and allowance of the case is solicited.

Respectfully submitted,

OBLON, SPIVAK, McCLELLAND,  
MAIER & NEUSTADT, P.C.



Norman F. Oblon  
Attorney of Record  
Registration No. 24,618

Daniel J. Pereira, Ph.D.  
Registration No. 45,518



**22850**

(703) 413-3000  
Fax #: (703)413-2220  
NFO/DJPER/js

I:\user\DJPER\205235US-PR.wpd

**Marked-Up Copy**

Serial No: \_\_\_\_\_

Amendment Filed on: \_\_\_\_\_

IN THE CLAIMS

Please amend the claims as follows:

3. (Amended) The ceramic heater according to Claim 1 [or 2]

wherein said ceramic substrate is at least one member selected from the group consisting of [among] a nitride ceramic, a carbide ceramic and an oxide ceramic.

6. (Amended) The ceramic heater according to Claim 4 [or 5]

wherein said nitride ceramic board has the form of a disk having a diameter of more than 150 mm.

7. (Amended) The ceramic heater according to Claim 4[, 5 or 6]

wherein the content of at least one element selected from the group consisting of Y, Li, Rb and Ca is not less than 0.1 weight %.

8. (Amended) The ceramic heater according to Claim 4[, 5 or 6]

wherein the content of at least one element selected from the group consisting of Na and B is not less than 0.05 ppm.

Claims 9-12 are new.

## CERAMIC HEATER

## TECHNICAL FIELD

The present invention relates to a ceramic heater which  
5 finds application in semiconductor industry and does not  
contaminate semiconductor wafers.

## BACKGROUND ART

Semiconductor devices are manufactured through a process  
10 which comprises coating a photosensitive resin as an etching  
resist on a semiconductor wafer and etching the uncoated area.

While the photosensitive resin is applied in the form of  
a liquid to the surface of a semiconductor wafer by means of  
a spin coater or the like, it must be dried after coating to  
15 dissipate the solvent etc. and the thus-coated semiconductor  
wafer is set on a heater and heated.

The conventional metallic heater heretofore used for this  
purpose comprises a heating element disposed on the reverse side  
of an aluminum plate.

20 However, such a metallic heater has the following  
disadvantages.

First, because it comprises a metallic material, the  
heater plate must be as thick as about 15 mm. This is because  
a thin metal plate undergoes thermal expansion on the heating  
mode to develop a warp and strains to damage or tip the  
25 semiconductor wafer placed on the plate. However, increasing  
the thickness of the heater plate leads to increases in heater  
weight and bulk.

Moreover, while the heater temperature is controlled by  
30 varying the voltage and current applied to the heating element,  
the great thickness of the metal plate causes the problem that  
the temperature of the heater plate cannot timely follow changes  
in voltage and current values, thus making temperature control  
difficult.

35 Therefore, Japanese Kokai Publication Hei-9-306642 and

Japanese Kokai Publication Hei-4-324276, for instance,  
proposed ceramic heaters each comprising a substrate board  
comprising AlN, which is a non-oxide ceramic material of high  
thermal conductivity and high strength, and a heating element  
5 formed internally of said substrate board.

Regarding such ceramic heaters, ceramics containing Y,  
Na, B, Li, Rb and/or Ca are superior in thermal conductivity  
and, therefore, are preferred for use in heaters. JP 2798570,  
inter alia, discloses a sintered aluminum nitride material  
10 containing Ca and Na and having a high thermal conductivity.

Meanwhile, in the ordinary ceramic heater, as described  
in Japanese Kokai Publication Hei-7-280462, the surface on  
which the heating element is formed is roughened while the  
surface on which a semiconductor wafer is to be placed and heated  
15 is flattened.

However, in a ceramic heater based on ceramics containing  
Y, Na, Ca and/or the like, if the surface on which a semiconductor  
wafer is to be placed is flat and smooth, the area of contact  
between the wafer and the ceramic heater is increased, with the  
20 result that the wafer is contaminated by thermal diffusion.

Moreover, even when the semiconductor wafer is not  
brought into direct contact with the heating surface of ceramic  
heater but held apart from the surface by means of support pins  
or the like and heated, the trouble of vaporized Y, Na, Ca, etc.  
25 being deposited on the semiconductor wafer is actually  
encountered.

A further disadvantage is that when a semiconductor wafer  
is held apart from the heating surface of the ceramic heater  
with supporting pins or the like and heated, the temperature  
30 distribution of the heating surface is reflected in the ceramic  
heater.

#### SUMMARY OF THE INVENTION

The inventors of the present invention explored into the  
35 above problems with the prior art and found that by roughening



the surface of a ceramic heater on which a wafer work such as a semiconductor wafer is placed or supported for heating (hereinafter referred to sometimes as the wafer-heating surface), the contact between the wafer-heating surface and the semiconductor wafer placed thereon can be made point contact, with the result that even when the ceramic board contains such impurities as Y and Na, the thermal diffusion of the impurities from the ceramic heater to the semiconductor wafer can be prevented. They further discovered that the vaporization of Y, Na, etc. from the wafer-heating surface can also be prevented by roughening said wafer-heating surface.

It was found that the failure of a semiconductor wafer to attain a uniform temperature when the semiconductor wafer is held apart from the heating surface during heating results from the turbulence of gas flow created in the clearance between the substrate, e.g. a semiconductor wafer, and the heating surface owing to the irregularities of said surface which cause a local accumulation of heat and further that the temperature distribution of the semiconductor wafer can be rendered more uniform by increasing the flatness of the heating surface to thereby preclude creation of said turbulence of gas flow. The inventors have accordingly perfected the present invention.

Thus, the ceramic heater according to the first aspect of the present invention comprises a ceramic substrate and a heating element disposed either on the surface or internally of said substrate, wherein the work-heating surface of the heater has a JIS B 0601 surface roughness of  $R_{max} = 0.05$  to  $200 \mu m$ .

The ceramic heater according to the second aspect of the present invention comprises a ceramic substrate and a heating element disposed either on the surface or internally of said substrate, wherein said ceramic substrate contains an element other than its dominant constituent elements and the work-heating surface of the heater has a JIS B 0601 surface roughness of  $R_{max} = 0.2$  to  $200 \mu m$ .

In the ceramic heaters according to said first and second aspects of the present invention, the ceramic substrate is preferably at least one member selected from the group consisting of a nitride ceramic, a carbide ceramic and an oxide ceramic.

The ceramic heater according to the third aspect of the present invention comprises a nitride ceramic substrate and a heating element disposed either on the surface or internally of said nitride ceramic substrate, wherein said nitride ceramic substrate contains an element other than its dominant constituent elements and the work-heating surface of the heater has a JIS B 0601 surface roughness of  $R_{\max} = 0.2$  to  $200 \mu\text{m}$ .

The ceramic heater according to the fourth aspect of the present invention comprises a nitride ceramic substrate and a heating element either on the surface or internally of said substrate, wherein said nitride ceramic substrate contains at least one additional element selected from the group consisting of Na, B, Y, Li, Rb and Ca and the heating surface thereof for heating a work has a JIS B 0601 surface roughness of  $R_{\max} = 0.2$  to  $200 \mu\text{m}$ .

In the ceramic heaters according to said third and fourth aspects of the present invention, the nitride ceramic substrate is preferably in the form of a disk having a diameter of more than 150 mm.

Furthermore, said ceramic substrate preferably contains not less than 0.1 weight % of at least one element selected from the group consisting of Y, Li, Rb and Ca and not less than 0.05 ppm of at least one element selected from the group consisting of Na and B.

#### BRIEF DESCRIPTION OF THE DRAWINGS

Fig. 1 is a schematic plan view of a ceramic heater as an embodiment of the invention.

Fig. 2 is a schematic cross-section view showing a portion of the ceramic heater illustrated in Fig. 1.

Fig. 3 is a schematic cross-section view showing an electrostatic chuck according to the invention, which comprises a ceramic heater provided with static electrodes.

Fig. 4 is a cross-section view of the same electrostatic chuck as taken along the line A-A of Fig. 3.

Fig. 5 (a) to (d) are schematic cross-section views showing the flow of manufacture of said electrostatic chuck.

Fig. 6 is a schematic cross-section view, on partly exaggerated scale, of a part of the ceramic heater equipped with supporting pins.

#### BRIEF DESCRIPTION OF NUMERIC SYMBOLS

	10	ceramic heater
	11	heater plate
15	11a	bottom face
	11b	heating surface
	12	heating element
	12a	metal cover layer
	13	external terminals
20	14	blind holes
	15	through holes
	16	support pin
	19	semiconductor wafer
	20,20a,20b	chuck positive electrode static layer
25	30,30a,30b	chuck negative electrode static layer
	40	ceramic dielectric film
	50	resistance heating element
	90	silicon wafer
	100	ceramic substrate
30	101	electrostatic chuck

#### DETAILED DESCRIPTION OF THE INVENTION

The ceramic heater according to the first aspect of the present invention comprises a ceramic substrate and a heating element disposed either on the surface or internally of said

substrate, wherein its work-heating surface has a JIS B 0601 surface roughness of  $R_{\max} = 0.05$  to  $200 \mu\text{m}$ .

The ceramic heater according to the second aspect of the present invention comprises a ceramic substrate and a heating element disposed either on the surface or internally of said substrate, wherein the ceramic substrate contains at least one additional element other than its dominant constituent elements and its work-heating surface has a JIS B 0601 surface roughness of  $R_{\max} = 0.2$  to  $200 \mu\text{m}$ .

The third and fourth aspects of the present invention are characterized by the defined raw material, heating surface roughness and elements in the ceramic substrate. Therefore, the following description is primarily directed to the first and second aspects of the invention and the other aspects of the invention will be additionally dealt with, where necessary.

The ceramic heater of the present invention is chiefly used for the heating of work-heating surface semiconductor wafers and the like, although it can be used for the purpose of heating other kinds of works as well.

Since the JIS B 0601 surface roughness of the ceramic heater according to the first aspect of the invention is  $R_{\max} = 0.05$  to  $200 \mu\text{m}$ , the work can be heated uniformly while it is held apart from its heating surface.

In the case of heating the work held apart from the heating surface, if the  $R_{\max}$  value of the heating surface exceeds  $200 \mu\text{m}$ , a turbulence of gas flow is created between the work and the heating surface so that the heat is locally built up to cause a temperature distribution in the work. On the other hand, if the  $R_{\max}$  value is less than  $0.05 \mu\text{m}$ , the temperature distribution of the heating surface is directly reflected on the work by radiant heat so that a temperature distribution is created. Thus, the range of  $R_{\max} = 0.05$  to  $200 \mu\text{m}$  is a characteristic range of surface roughness suited for the heating mode in which the work is held apart from the heating surface and heated in a gaseous heat transfer medium.

The gaseous heat transfer medium is preferably at least one species selected from among air, inert gases and reactive gases. The preferred inert gases are carbon dioxide gas, nitrogen gas and argon gas. As said reactive gases, halogen gases and  $\text{CF}_4$  can be mentioned.

The preferred roughness  $R_{\text{max}}$  of the heating surface is 0.2 to 100  $\mu\text{m}$ . Within this range, the vaporization of impurities from the ceramic substrate can be successfully controlled.

In order to hold the work apart from the heating surface, supporting pins 18 or the like are set securely in the through-holes or cavities of ceramic substrate 11 as shown in Fig. 6.

The preferred distance between the work and the heating surface is 1 to 5000  $\mu\text{m}$ . If the distance is less than 1  $\mu\text{m}$ , the temperature distribution of the heating surface will be reflected on the work and, moreover, the undulation of the heating surface may cause mutual contact of the work and the heating surface. On the other hand, if the distance exceeds 5000  $\mu\text{m}$ , the temperature difference between the heating surface and the work will become too large.

In the ceramic heater according to the second aspect of the present invention, the ceramic substrate contains an element other than its dominant constituent elements. However, since the roughness of the heating surface is set at  $R_{\text{max}} = 0.2$  to 200  $\mu\text{m}$ , the work is protected against contamination by the thermal diffusion or evaporation of impurities from the ceramic substrate, particularly in the heating mode where the work is directly set on the heating surface and even in the heating mode where the work is heated while it is held apart at a given distance from the heating surface.

If the  $R_{\text{max}}$  value of the heating surface is less than 0.2  $\mu\text{m}$ , the area of contact between the heating surface and the work will be so large that said contamination with impurities diffusing into the work tends to take place. On the other hand,

if the  $R_{max}$  value exceeds  $200\text{ }\mu\text{m}$ , the mean distance between the work and the heating surface will become too large to heat the work sufficiently.

Furthermore, even in the mode of heating the work held  
 5 apart from the heating surface, the area of contact between the gaseous heat transfer medium and the heating surface is sufficiently large when the  $R_{max}$  value is not less than  $0.2\text{ }\mu\text{m}$ , so that the heat causative of evaporation of impurities from the surface of the ceramic substrate is effectively deprived  
 10 of by said heat transfer medium, with the consequence that the work contamination is minimized.

Thus, the JIS B 0601 surface roughness of  $R_{max} = 0.2$  to  $200\text{ }\mu\text{m}$  for the work-heating surface is a characteristic range in which the work can be successfully heated without  
 15 contaminating the work.

The ceramics which can be used in the practice of the present invention includes nitride ceramics, carbide ceramics and oxide ceramics.

The preferred nitride ceramic is at least one member  
 20 selected from among aluminum nitride, silicon nitride, boron nitride and titanium nitride.

Among these, aluminum nitride is the most preferred, for its thermal conductivity,  $180\text{ W/m}\cdot\text{K}$ , is the highest of all.

The preferred carbide ceramic is at least one member  
 25 selected from among silicon carbide, titanium carbide, boron carbide, tantalum carbide, tungsten carbide and zirconium carbide.

The oxide ceramic is preferably at least one member selected from among alumina, silica, zirconia, mullite,  
 30 cordierite and berryllia.

When the ceramic substrate is electrically conductive, an insulating layer may be provided on its surface adjacent to the heating element. The insulating layer is preferably made of  $\text{SiO}_2$ ,  $\text{Al}_2\text{O}_3$  or the like and the preferred thickness of this  
 35 layer is  $0.1$  to  $2000\text{ }\mu\text{m}$ .

When the heating element is a conductor wire, the surface of the conductor is covered with an insulating layer.

The particularly preferred ceramics are nitride ceramics and carbide ceramics, for these ceramics are high in thermal conductivity and, hence, superior in heat controllability. If the heat output of the heating element is not uniform, this uneven distribution of heat tends to be directly reflected on the heating surface when the thermal conductivity of the substrate material is high. Therefore, the effect of the surface roughness control according to the present invention can be exploited to advantage.

In the case of using the nitride ceramic as the ceramic substrate, said nitride ceramic board contains an element other than constituent elements of nitride ceramics, specifically at least one member selected from Y, Na, B, Li, Rb and Ca, and, as such, has good thermal conductivity, with the result that the surface temperature of the heater plate may timely follow the change in temperature of the heating element, thus enabling efficient control of the wafer-heating surface temperature.

Furthermore, since the wafer-heating surface has been roughened as mentioned above, the contact between the wafer-heating surface and the semiconductor wafer placed thereon can be made point contact so that the thermal diffusion of impurities (Y and other impurities) from the ceramic heater to the semiconductor wafer can be inhibited.

Moreover, even when a semiconductor wafer is supported out of contact by support pins as illustrated in Fig. 2, the impurity elements other than the constituent elements of nitride ceramics, such as Y, Na, B, Li, Rb, Ca, Al, etc. (hereinafter referred to as Y and other impurities) will diffuse out and become vaporized at an elevated temperature to contaminate the semiconductor wafer unless the wafer-heating surface is roughened. Particularly when the ceramic heater is used in a sputtering line, for instance, where the heating operation is carried out in a vacuum, Y and other impurities

are more readily vaporized.

However, in the present invention wherein the wafer-heating surface has been roughened, Y and other impurities are not vaporized from the surface so that the contamination of the semiconductor wafer can be precluded.

It remains not to be fully known why the formation of a rough surface is effective in preventing vaporization of the elements other than the constituent elements of ceramics such as Y etc., but it is supposed that because the roughened wafer-heating surface has a function like the function of radiator fins and the surface is deprived of the heat causing vaporization of Y and other impurities.

When a semiconductor wafer is heated in the condition supported by support pins, the preferred distance between the semiconductor wafer and the ceramics is preferably 1 to 5000  $\mu\text{m}$ , more preferably 5 to 500  $\mu\text{m}$ . If they are too widely apart, the heating effect will be sacrificed, while an excessively close clearance results in a more or less faithful reflection of the surface temperature distribution of the ceramic board so that the semiconductor wafer cannot be uniformly heated.

The ceramic heater of the present invention comprises a heating element formed on the surface or internally of a nitride ceramic board.

Fig. 1 is a schematic plan view of a ceramic heater embodying the present invention and Fig. 2 is a sectional view showing a portion of the same heater on exaggerated scale.

A ceramic substrate 11 (hereinafter also referred to as a heater plate) is formed in the shape of a disk and a heating element 12 is formed in a pattern of concentric circles on the bottom side of the heater plate 11 so that the wafer-heating surface 11b of the nitride ceramic board (hereinafter referred to sometimes as heater plate) may be heated to a uniform temperature over its entire area. The heating element need not be a patterned element but may for example be a Peltier element.

The heating element 12 mentioned above actually comprises



a plurality of concentric patterns of double-circles, each of said double-circles forming a single conductor and each of the patterns is connected, at both ends, to external terminals 13, one of which functions as an input terminal, with the other as an output terminal, each via a metal cover layer 12a. In addition, the heating element 12 is provided, near its center, with through holes 15 for accepting said support pins 16 adapted to support a semiconductor wafer and further with blind holes 14 for accepting temperature probes.

The ceramic heater 10 shown in Figs. 1 and 2 is provided with said heating element 12 on the bottom side of the heater plate 11. However, it may be formed internally of the heater plate 11.

The constituent members of the ceramic heater of the present invention are now described in further detail. In the following description, a nitride ceramic is taken as an example but the description applies to a carbide ceramic or an oxide ceramic as well.

In the ceramic heater 10 of the present invention, the heater plate 11 comprises nitride ceramics as the material of heater plate. This is because the particular material is lower than metal in the coefficient of thermal expansion and hence does not undergo curling or warping on heating even when its thickness is reduced, thus enabling reductions in both thickness and weight of the heater plate 11.

Furthermore, because of the high thermal conductivity and reduced thickness of the heater plate 11, the surface temperature of the heater plate 11 readily follows the temperature change of the heating element.

Stated differently, the temperature of the heating element can be adjusted by varying the voltage and current values to thereby control the surface temperature of the heater plate 11.

The heater plate 11 contains at least one element selected from Y, Na, B, Li, Rb and Ca.

Among them, Y, Li, Rb and Ca are generally added in the oxide form as sintering aids. In order that the thermal conductivity of the heater plate 11 may be as high as 130 to 200 W/m·K, the amount of Y, Li, Rb and/or Ca is preferably 0.1 to 5 weight %.

If the content of Y and other impurities is less than 0.1 weight %, no sufficient compaction will be attained in sintering, making it difficult to insure a high thermal conductivity.

Na and B are segregated in grain boundaries to contribute to an improved thermal conductivity.

The preferred amount of Na and B is 0.05 to 50 ppm. If the amount is less than 0.05 ppm, the thermal conductivity will not be enhanced.

These elements inherently occur in the starting material powder but, if necessary, a compound of Na and a compound of Ca, such as  $\text{NaCO}_3$  and  $\text{CaCO}_3$ , may be added to the powder to bring their content into the above-mentioned range.

As the sintered aid for carbide ceramics,  $\text{B}_4\text{C}$ ,  $\text{AlN}$ , etc. can be mentioned, and the impurities may for example be Al and B. The amount of Al and/or B in the ceramic substrate is 0.05 ppm to 0.5 weight %.

The heating surface of said heater plate 11 has been roughened to the JIS B 0601 surface roughness of  $R_{\text{max}} = 0.05$  to  $200 \mu\text{m}$  for preventing a turbulence of heat transfer medium in the heating mode where the work is held apart from the heating surface during heating in the heat transfer medium.

In this mode where the work is held apart from the heating surface during heating, if the roughness of the heating surface is greater than  $R_{\text{max}} = 200 \mu\text{m}$ , a turbulence of gas is created between the work and the heating surface and the consequent local accumulation of heat causes a temperature distribution in the work. On the other hand, if the surface roughness is less than  $R_{\text{max}} = 0.05 \mu\text{m}$ , the temperature distribution of the heating surface will be directly reflected on the work by radiant heat, thus causing a temperature distribution in the

work. The preferred surface roughness is  $R_{\max} = 0.2$  to  $100$ .

In the present invention, the heating surface of said heater plate 11 is roughened to a JIS B 0601 roughness of  $R_{\max} = 0.2$  to  $200 \mu\text{m}$  for the purpose of preventing diffusion of impurities into the semiconductor wafer.

If the surface roughness is less than  $R_{\max} = 0.2 \mu\text{m}$ , the mode of contact will be plane contact so that the diffusion of impurities into the semiconductor wafer may hardly be prevented. On the other hand, if the surface roughness exceeds  $R_{\max} = 200 \mu\text{m}$ , the wafer will not be sufficiently heated, not to speak of the difficulty of roughening, for probably because of the increased clearance between the wafer and the wafer-mounting surface, the amount of radiant heat or the heat propagated through the air and reaching the wafer is diminished.

Moreover, in the mode of use of the heater in which the wafer is supported by said support pins and heated without contacting, too, a heating surface with a degree of roughness less than  $R_{\max} = 0.2 \mu\text{m}$  is not effective in suppressing the vaporization of Y and other impurities. On the other hand, if the degree of surface roughness is over  $R_{\max} = 200 \mu\text{m}$ , the effective area of the heating surface will be increased to rather cause a greater amount of vaporization. Thus, the above  $R_{\max}$  range is effective in inhibiting the vaporization of Y and other impurities.

The preferred surface roughness is  $R_{\max} = 1$  to  $20 \mu\text{m}$ , for within this range, contact with the wafer does not easily give off dust particles.

The method for roughening the surface is not particularly restricted but may for example be the sand-blasting method in which alumina, SiC, glass, zirconia or other solid particles are blasted against the wafer-heating surface.

The preferred thickness of the heater plate 11 of the above ceramic heater is  $0.5$  to  $5 \text{ mm}$ . If the thickness is less than  $0.5 \text{ mm}$ , the plate will be liable to be destroyed because of reduced strength. On the other hand, if the thickness is

larger than 5 mm, the conduction of heat will be poor so that the heating efficiency will be sacrificed.

In the ceramic heater of the present invention, the geometry of said nitride ceramic board is not particularly restricted. While it may for example be elliptical in plan view, a disk-shaped board such as the one illustrated in Fig. 1 is preferred. When the nitride ceramic board is disk-shaped, its diameter is preferably more than 150 mm, more preferably not less than 200 mm, and still more preferably not less than 300 mm. This is because semiconductor wafers in popular use today are not less than 8 inches (ca 200 mm) in diameter and it is expected that the majority of the next-generation semiconductor wafers will be still larger, i.e. 12 inches (300 mm) or more in diameter.

The heating element 12 formed on the surface or internally of said nitride ceramic board is preferably divided in at least two circuits. By dividing the circuitry, the amount of heat generation can be adjusted by controlling the power input to the respective component circuits, with the result that the temperature of the wafer-heating surface can be accordingly controlled.

The pattern of the heating element 12 may for example be a set of concentric circles, a vortex, a set of eccentric circles, or a serpentine form but the concentric pattern illustrated in Fig. 1 is preferred because it insures a uniform temperature distribution over the whole of the heater plate.

When the heating element 12 is to be formed on the surface of the heater plate 11, it is preferable to use the method which comprises coating the surface of the heater plate 11 with a conductive paste containing a metal powder in a predetermined circuit pattern and baking the resulting conductor paste layer to sinter the metal powder on the surface of the heater plate 11. It is sufficient that this sintering be effected to the extent of the metal particles being fused to each other and with the ceramics.

When the heating element is formed on the surface of the heater plate 11, the thickness of the heating element is preferably 1 to 30  $\mu\text{m}$ , more preferably 1 to 10  $\mu\text{m}$ . On the other hand, when the heating element is formed internally of the  
 5 heater plate 11, its thickness is preferably 1 to 50  $\mu\text{m}$ .

Moreover, when the heating element is formed on the surface of the heating element 11, the conductor width of the heating element is preferably 0.1 to 20 mm, more preferably 0.1 to 5 mm. When the heating element is formed internally of the  
 10 heater plate 11, the conductor width of the heating element is 5 to 20  $\mu\text{m}$ .

The resistance value of the heating element 12 can be freely adjusted by varying the conductor thickness and width but the above-mentioned ranges are most useful for practical  
 15 purposes. The thinner and finer the width of the heating element is, the greater is its resistance value. The heating element 12 should be greater in thickness and width when it is formed internally of the heater plate 11. When the heating element 12 is formed internally, the distance between the  
 20 heating surface and the heating element 12 is comparatively short, with the result that the temperature uniformity of the heating surface tends to be poor, thus making it necessary to increase the width of the heating element. Moreover, when the heating element 12 is to be disposed internally, its adhesion  
 25 to nitride ceramics need not be taken into consideration, so that a high-melting metal such as tungsten or molybdenum or the carbide thereof can be used to increase the resistance and, therefore, the thickness itself can be increased for the purpose of preventing a disconnection trouble, for instance. From  
 30 these considerations, the conductor thickness and width of the heating element 12 are preferably controlled within the above-mentioned ranges.

The heating element 12 may be rectangular or elliptic in section but preferably has a flat surface. The flat surface  
 35 is more efficient in the radiation of heat toward the

wafer-heating surface and is less liable to cause a temperature gradient.

The aspect ratio of the cross-section (width of heating element/thickness of heating element) is preferably 10 to 5000.

5 Because, by controlling said aspect ratio within this range, the resistance value of the heating element 12 can be increased and the temperature uniformity of the heating surface can be insured.

10 Assuming that the thickness of the heating element 12 is constant, the use of an aspect ratio smaller than the above range leads to a relatively reduced propagation of heat from the heater plate 11 in the wafer-heating direction so that a distribution of heat simulating the pattern of the heating element 12 is reproduced on the heating surface. Conversely, 15 if the aspect ratio is too large, a relatively high temperature will prevail in the area immediately above the center of the heating element 12 so that a thermal distribution more or less simulating the pattern of the heating element 12 is likewise produced on the heating surface. Therefore, in consideration 20 of temperature distribution, the aspect ratio of the cross-section is preferably confined to the range of 10 to 5000.

When the heating element 12 is to be formed on the surface of the heater plate 11, the aspect ratio is preferably 10 to 200 and when the heating element 12 is to be formed internally 25 of the heater plate 11, the preferred aspect ratio is 200 to 5000.

That a larger aspect ratio is preferred when the heating element 12 is formed internally of the heater plate 11 is because, as the heating element 12 is internally disposed, the distance 30 between the heating surface and the heating element 12 is relatively decreased so that the uniformity of surface temperature distribution is also reduced, thus requiring a more flat geometry for the heating element 12 as such.

When the heating element 12 is to be formed in an offset 35 position within the heater plate 11, it is preferably formed

in a position close to the surface (bottom surface) opposite to the heating surface of the heater plate 11 and within the range of 50% through 99% of the distance from the heating surface to said bottom surface.

5        If the amount of offset is less than 50%, the heating element 12 will be too close to the heating surface so that a temperature gradient tends to form on the heating surface. Conversely, if the amount of offset is over 99%, the heater plate 11 itself tends to warp to destroy the semiconductor wafer.

10       Furthermore, when the heating element 12 is formed internally of the heater plate 11, the heating element may be divided in a plurality of layers. In this case, the patterns of the respective layers are preferably disposed in mutually complementary relation so that, when viewed from above the  
15       water-heating surface, the whole heating element 12 can be observed without overlaps over the entire field of view. Typical of such layout is a staggered arrangement of the patterns.

      It is also allowable to form the heating element 12  
20       internally of the heater plate 11, with the heating element 12 locally exposed.

      The conductor paste is not particularly restricted but is preferably a composition containing not only the metal or conductive ceramic powder required for electrical conductivity  
25       but also a resin, a solvent, a thickener and other additives.

      The metal powder mentioned above is preferably a powder of, for example, a noble metal (gold, silver, platinum, palladium), lead, tungsten, molybdenum or nickel. These metals can be used singly or in a combination of two or more  
30       species. Those metals are comparatively resistant to oxidation and have sufficiently high resistance to generate heat.

      The conductive ceramic powder includes but is not limited to a powder of tungsten carbide or molybdenum carbide. Those  
35       powders can be used singly or in a combination of two or more

species.

The preferred particle diameter of said metal or conductive ceramic powder is 0.1 to 100  $\mu\text{m}$ . If the powder is finer than 0.1  $\mu\text{m}$ , it will be ready to be oxidized. On the other hand, if the limit of 100  $\mu\text{m}$  is exceeded, the powder will not be easily sintered and the resistance value will be increased.

The metal powder mentioned above may be spherical or flaky. A mixture of spherical and flaky powders may also be employed. When the metal powder is flaky or a mixture of spherical and flaky particles, the metal oxide added is held more effectively between the metal particles, with the consequent advantage that a firmer adhesion can be assured between the heating element 12 and the nitride ceramics and that the resistance value can be increased.

The resin for use in the conductor paste includes but is not limited to epoxy resin and phenolic resin. The solvent may for example be isopropyl alcohol. The thickener may for example be cellulose.

As mentioned above, the conductor paste is preferably prepared by adding metal oxides to the metal powder so that the heating element 12 will be a sintered body composed of the metal and metal oxide powders. Thus, by sintering the metal oxide together with the metal powder, an intimate bond can be realized between the heater plate nitride ceramics and the metal powder.

It remains to be fully clarified as yet why incorporation of the metal oxide results in an improved strength of bonding to the nitride ceramics but it may be suggested that since the surface of the metal particles and that of the nitride ceramics have been slightly oxidized and the resulting oxide films are integrally sintered together with the intermediary of the metal oxides added, thus causing an intimate adhesion of the metal powder to the nitride ceramics.

The metal oxide mentioned above is preferably at least one member selected from the group consisting of lead oxide,



zinc oxide, silica, boron oxide ( $B_2O_3$ ), alumina, yttria and titania.

These oxides are capable of improving the adhesion between the metal powder and nitride ceramics without  
5 increasing the resistance value of the heating element 12.

The proportions of said lead oxide, zinc oxide, silica, boron oxide ( $B_2O_3$ ), alumina, yttria and titania in each 100 weight parts of the total metal oxide are 1 to 10 (weight parts; the same applies below) for lead oxide, 1 to 30 for silica, 5  
10 to 50 for boron oxide, 20 to 70 for zinc oxide, 1 to 10 for alumina, 1 to 50 for yttria, and 1 to 50 for titania. The total of these oxides is preferably not more than 100 weight parts.

By adjusting the amount of the oxide within the above range, the bonding to nitride ceramics, in particular, can be  
15 improved.

The addition amount of said metal oxide relative to the metal powder is preferably not less than 0.1 weight % and less than 10 weight %. Moreover, the area resistivity of a heating element 12 formed with a conductor paste of the above  
20 composition is preferably 1 to 45  $m\Omega/\square$ .

If the area resistivity exceeds 45  $m\Omega/\square$ , the generation of heat will be too great in relation to the amount of voltage applied so that the amount of heat can hardly be controlled in the case of a heater plate carrying the heating element 12 on  
25 the surface. If the addition amount of said metal oxide is not less than 10 weight %, the area resistivity will exceed 50  $m\Omega/\square$  and the excessively increased heat generation will make temperature control difficult and the uniformity of temperature distribution will be sacrificed.

When the heating element 12 is formed on the surface of the heater plate 11, the surface of the heating element 12 is preferably provided with a metal cover layer 12a for preventing oxidation of the sintered metal and consequent change in resistance value. The thickness of the metal cover layer 12a  
30 thus formed is preferably 0.1 to 10  $\mu m$ .  
35

The metal for use in the formation of said metal cover layer 12a is not particularly restricted only provided that it is a non-oxidizable metal but may for example be gold, silver, palladium, platinum or nickel. These metals can be used alone or in a combination of two or more species. Among the metals mentioned above, nickel is particularly preferred.

While the heating element 12 must, of course, be provided with terminals for electrical connection to a power source and these terminals are attached to the heating element 12 via a solder, nickel is capable of inhibiting thermal diffusion of the solder. As the connecting terminal means, external terminals 13 comprising Koval<sup>®</sup>, for instance, can be used.

When the heating element 12 is formed internally of the heater plate 11, the surface of the heating element is not oxidized and, therefore, need not be covered. When the heating element 12 is formed internally of the heater plate 11, the heating element may be left partially exposed and provided with plated-through holes for accepting and securing in position said external terminals.

As the solder for use in connecting the external terminals 13, various alloys such as silver-lead, lead-tin, bismuth-tin, etc. can be used. The preferred thickness of the solder layer is 0.1 to 50  $\mu\text{m}$ . Within this range, firm solder connections can be obtained.

Furthermore, as illustrated in Fig. 2, the heater plate 11 may be provided with through holes 15 so that, by inserting support pins (not shown) into the through holes 15, the semiconductor wafer may be delivered to a transfer machine (not shown) or received from such a machine.

The method of manufacturing the ceramic heater of the present invention is now described.

First, the process for fabrication of a ceramic heater comprising a heating element formed on the bottom surface of a heater plate 11 (Figs. 1 and 2) is described. In the following description, a nitride ceramic is taken as an example but the

description applies to a carbide ceramic or an oxide ceramic as well.

(1) Fabrication of a heater plate

The nitride ceramic powder described above, e.g. an aluminum nitride ceramic powder, is formulated with optional sintering aids, such as yttria ( $Y_2O_3$ ), calcia (CaO), lithium oxide ( $Li_2O$ ), rubidium oxide ( $Rb_2O$ ), etc., compounds of Y, Na, Ca, Li, Rb and B, a binder and other components to prepare a slurry. This slurry is spray-dried to give granules, which are then placed in a metal mold or the like and compressed into a green board.

Then, the green board is optionally formed with through holes for accepting the support pins for supporting a semiconductor wafer and with blind holes in which temperature probes such as thermocouples are to be embedded.

Then, this green board is sintered by heating to provide a ceramic board. A heater plate 11 is then prepared by machining this ceramic board. As an alternative, the board may be formed to the specified size so that it may directly serve as a heater plate 11. By conducting a sintering operation under pressure, a cavity-free heater plate 11 can be fabricated. The heating temperature may be any temperature not below the sintering point but, in the case of nitride ceramics, a temperature within the range of 1000 to 2500 °C is used. Thereafter, at least the wafer-heating surface is roughened by sandblasting, for instance. If desired, both sides may be similarly roughened.

(2) Printing the heater plate with a conductor paste

The conductor paste is generally a highly viscous fluid comprising a metal powder, a resin and a solvent. The conductor paste layer is formed by printing the board with said conductor paste in the pattern corresponding to a heating element by the screen printing technique, for instance. Since the heating element is required to heat the heater plate uniformly over the whole surface, it is preferably printed in a concentric circular pattern as illustrated in Fig. 1.

It is preferable that the conductor paste layer be formed in such a manner that the heating element 12 after sintering will present a rectangular section and a flat surface.

(3) Baking the conductor paste

5       The conductor paste layer formed on the bottom surface of the heater plate 11 is then heated to remove the resin and solvent and bake the metal powder onto the bottom surface of the heater plate 11 to complete a heating element 12. The heating temperature is preferably 500 to 1000 °C.

10       When said metal oxide has been incorporated in the conductor paste, the metal powder, heater plate and metal oxide are integrally sintered to provide an improved bond between the heating element and heater plate.

(4) Formation of a metal cover layer

15       The surface of the heating element 12 is preferably provided with the metal cover layer 12a. The metal cover layer 12a can be formed by, for example, electrolytic plating, electroless plating or sputtering but, for mass production, electroless plating is the most suitable of all.

20       (5) Attaching terminals etc.

To ends of each circuit pattern of the heating element 12, terminals (external terminals 13) for electrical connection to a power source are attached by soldering. In addition, thermocouples are inserted and secured in the blind holes 14  
25       with a silver or gold brazing material and sealed with a heat-resistant resin such as a polyimide resin to complete the manufacture of a ceramic heater.

The method of manufacturing a ceramic heater comprising a heating element 12 formed internally of a heater plate 11 is  
30       now described.

(1) Fabrication of a heater plate

First, the nitride ceramic powder is mixed with the binder, solvent, etc. to prepare a paste, and using the paste, a green sheet is formed.

35       The nitride ceramic powder as mentioned above includes

an aluminum nitride powder, and where necessary, sintering aids such as yttria as well as compounds of Na and Ca may be added.

The binder is preferably at least one member selected from the group consisting of acrylic binder, ethylcellulose,  
5 butylcellosolve and polyvinyl alcohol.

The solvent is preferably at least one member selected from the group consisting of  $\alpha$ -terpineol and glycol.

The paste obtained by compounding those components is molded by the doctor blade method to provide said green sheet.

10 The preferred thickness of the green sheet is 0.1 to 5 mm.

Then, where necessary, the green sheet is formed with means to serve as through holes 15 for accepting support pins for supporting a semiconductor wafer, means to serve as blind holes in which temperature probes such as thermocouples are to be embedded, and means for serving as plated-through holes 18 for connecting the heating element to external terminal pins. This processing may be carried out after formation of a green sheet laminate to be described below or after sintering of the  
20 laminate.

(2) Printing the green sheet with the conductive paste

On the green sheet, the conductive paste containing a metal or conductive ceramic powder is printed for the formation of a heating element.

25 The conductive paste contains a metal or conductive ceramic powder.

The mean particle diameter of tungsten or molybdenum powder is preferably 0.1 to 5  $\mu\text{m}$ . If the mean particle diameter is less than 0.1  $\mu\text{m}$  or over 5  $\mu\text{m}$ , the printing of the conductive  
30 paste will be hardly carried out.

The conductive paste may for example be a composition (paste) comprising 85 to 87 weight parts of a metal or conductive ceramic powder, 1.5 to 10 weight parts of at least one kind of binder selected from the group consisting of acrylic binder,  
35 ethylcellulose, butylcellosolve and polyvinyl alcohol, and 1.5

to 10 weight parts of at least one kind of solvent selected from the group consisting of  $\alpha$ -terpineol and glycol.

(3) Laminating green sheets

5 The green sheets not printed with the conductive paste are laminated on both sides of the green sheet printed with the conductive paste.

10 In this step, it is arranged so that the number of green sheets to be laminated on the top side is larger than the number of green sheets to be laminated on the bottom side so that the heating element will be situated offset in the direction of the bottom side.

Specifically, the preferred number of green sheets laminated is 20 to 50 on the top side and 5 to 20 on the bottom side.

15 (4) Sintering the green sheet laminate

The green sheet laminate is hot-pressed to sinter the green sheets and the conductive paste within the laminate.

20 The heating temperature is preferably 1000 to 2000 °C and the pressure to be applied is preferably 100 to 200 kg/cm<sup>2</sup>. The heating is performed in an inert gas atmosphere. The inert gas may for example be argon gas or nitrogen gas.

25 Then, roughening treatment is carried out by polishing at least one heating surface and, where necessary, further subjected to sand blasting or the like. This roughening treatment may be carried out on both sides.

30 The blind holes for accepting temperature probes may be formed after this sintering operation. The blind holes can be formed by blasting, e.g. sandblasting, after surface abrasion. In addition, external terminals 13 are connected to the plated-through holes for electrical connection to the internal heating element, followed by heating for reflow. The heating temperature is preferably 200 to 500 °C.

35 Then, thermocouples as temperature probes are embedded with the aid of a silver or gold brazing material and sealed with a heat-resistant resin such as a polyimide resin to

complete the manufacture of a ceramic heater.

The operating temperature range of the heater of the present invention is 150 to 800 °C. As to the operating atmosphere, the heater can be used under atmospheric pressure or subatmospheric pressure. Under these conditions, the present invention expresses a particularly remarkable effect.

The ceramic heater of the present invention can be provided with static electrodes required for use as an electrostatic chuck, or with a chuck top surface conductor layer required for use as a wafer prober.

The construction of said electrostatic chuck is now briefly described.

Fig. 3 is a schematic longitudinal section view showing the electrostatic chuck and Fig. 4 is a sectional view of the electrostatic chuck as taken along the line A-A of Fig. 3.

In this electrostatic chuck 101, a chuck positive and a chuck negative electrode layers 20, 30 are formed internally of an aluminum nitride board 100 and a ceramic dielectric film 40 further is formed. In addition, a resistance heating element 50 is formed internally of the aluminum nitride board 100 so that a silicon wafer 90 can be heated. Where necessary, the aluminum nitride board 100 may have a RF electrode embedded.

As illustrated in Fig. 4, the electrostatic chuck 101 is usually formed in a circular configuration in plan view, and as disposed internally of the aluminum nitride board 100, the chuck positive electrode static layer 20 comprising a semi-circular segment 20a and a comb-shaped segment 20b and the chuck negative electrode static layer 30 similarly comprising a semi-circular segment 30a and a comb-shaped segment 30b are arranged face-to-face in such a manner that the teeth of the two comb-shaped segments 20b, 30b are in staggered arrangement.

For operating this electrostatic chuck, the chuck positive and chuck negative electrode static layers 20 and 30 are connected to the + and - terminals, respectively, of a DC source and DC voltage is applied. Thereupon, the semiconductor

wafer set on the electrostatic chuck is firmly attracted electrostatically.

#### BEST MODE FOR CARRYING OUT THE INVENTION

5 The present invention is now described in further detail.

(Example 1)

10 (1) A composition comprising 100 weight parts of aluminum nitride powder (manufactured by Tokuyama Co.; average particle diameter:  $1.1\ \mu\text{m}$ ) containing Na and B in the concentrations of 0.1 ppm and 0.8 ppm, respectively, 4 weight parts of yttria (average particle diameter:  $0.4\ \mu\text{m}$ ), 12 weight parts of acrylic binder and the remainder of alcohol was spray-dried to prepare a granular powder.

15 (2) This granular powder was set in a metal mold and formed into a green sheet. This green sheet was drilled to form openings corresponding to through holes 15 for accepting semiconductor wafer-support pins and openings corresponding to blind holes 14 for embedding thermocouples ( $1.1\ \text{mm dia.} \times 2\ \text{mm deep}$ ).

20 (3) The processed green sheet was then hot-pressed at  $1800\ ^\circ\text{C}$  and  $200\ \text{kg/cm}^2$  to provide a 3 mm-thick aluminum nitride sheet.

From this sheet, a disk having a diameter of 210 mm was cut out to provide a ceramic disk (heater plate 11).

25 Both sides of this heater plate 11 were sandblasted with an alumina powder having an average particle size of  $5\ \mu\text{m}$  to form surface irregularities corresponding to  $R_{\text{max}} = 6\ \mu\text{m}$  according to JIS B 0601.

30 (4) On the heater plate 11 processed in (3) above, a conductor paste was applied by screen printing. The printing pattern was a concentric circular pattern as shown in Fig. 1.

The conductor paste used was Solbest PS603D available from Tokuriki Kagaku Kenkyusho, which is in common use for the formation of plated-through holes in printed circuit boards.

35 This conductor paste is a silver-lead paste containing,



based on 100 weight parts of silver, 7.5 weight parts of metal oxide comprising lead oxide (5 wt. %), zinc oxide (55 wt. %), silica (10 wt. %), boron oxide (25 wt. %) and alumina (5 wt. %). The silver powder was a flaky powder having an average particle diameter of 4.5  $\mu\text{m}$ .

(5) The heater plate 11 printed with the conductor paste as above was then heated at 780 °C to sinter the silver and lead in the conductor paste and bake them onto the heater plate 11 to form a heating element 12. The heating element composed of silver and lead was 5  $\mu\text{m}$  thick  $\times$  2.4 mm wide and had an area resistivity of 7.7 m $\Omega$ /□.

(6) The heater plate 11 thus fabricated in (5) above was dipped in an electroless nickel plating bath comprising an aqueous solution of nickel sulfate 80 g/l, sodium hypophosphite 24 g/l, sodium acetate 12 g/l, boric acid 8 g/l and ammonium chloride 6 g/l to deposit a 1  $\mu\text{m}$ -thick metal cover layer (nickel layer) 12a on the surface of the silver-lead heating element 12.

(7) The parts to which external terminals 13 are to be attached for electrical connection to a power source are printed with a silver-lead soldering paste (manufactured by Tanaka Noble Metals) by the screen printing technique to form a solder layer.

On the solder layer, external terminals 13 of Koval<sup>®</sup> were placed, followed by heating for reflow at 420 °C. In this manner, the external terminals 13 were secured to the surface of the heating element 12.

(8) Thermocouples for temperature control were fixed in position with a 81.7 Au - 18.3 Ni gold brazing material (fusion by heating at 1030 °C) to provide a ceramic heater 10.

### 30. (Comparative Example 1)

(1) Except that the prepared heater plate was not sandblasted to roughen both sides, the procedure of Example 1 was otherwise repeated to manufacture a ceramic heater. Because the roughening treatment was not performed, the surface roughness was  $R_{\text{max}} = 0.1 \mu\text{m}$  according to JIS B 0601.

(Comparative Example 2)

- 5 (1) Except that yttria was not added and Mitsui Toatsu's MAN-5 (Na and B at a concentration of less than 0.1 ppm) was used as the aluminum nitride powder in the fabrication of the heater plate, the procedure of Example 1 was otherwise repeated to manufacture a ceramic heater.

(Comparative Example 3)

- 10 (1) After preparation of a heater plate, both sides of the plate were roughened by alumina (mean part. dia.: 250  $\mu\text{m}$ ) sandblasting to  $R_{\text{max}} = 210 \mu\text{m}$  according to JIS B 0601.

(Example 2)

- 15 A composition of 100 weight parts of aluminum nitride powder (manufactured by Tokuyama Co.; average particle diameter: 1.1  $\mu\text{m}$ ) containing Na and B in the concentrations of 0.1 ppm and 0.8 ppm, respectively, 5 weight parts of CaO (average particle diameter: 0.3  $\mu\text{m}$ ), 12 weight parts of acrylic  
20 binder and the remainder of alcohol was spray-dried to prepare a granular powder. Otherwise, the procedure of Example 1 was faithfully repeated to manufacture a ceramic heater.

(Example 3)

- 25 A composition of 100 weight parts of aluminum nitride powder (manufactured by Tokuyama Co.; average particle diameter: 1.1  $\mu\text{m}$ ) containing Na and B in the concentrations of 0.1 ppm and 0.8 ppm, respectively, 3 weight parts of  $\text{Li}_2\text{O}$  (average particle diameter: 0.3  $\mu\text{m}$ ), 2 weight parts of  $\text{Pb}_2\text{O}$   
30 (average particle diameter: 0.3  $\mu\text{m}$ ), 12 weight parts of acrylic binder and the remainder of alcohol was spray-dried to prepare a granular powder. Otherwise, the procedure of Example 1 was faithfully repeated to manufacture a ceramic heater.

- 35 Then, the surface of this heater plate 11 was polished with a 0.5  $\mu\text{m}$ -diamond paste to  $R_{\text{max}} = 0.8 \mu\text{m}$  and, thereafter,

a ceramic heater was fabricated as in Example 1.

(Example 4)

The procedure of Example 1 was substantially repeated.

- 5 However, the external terminals were attached using a Ni-Au brazing material which was caused to reflow at 1030 °C. In addition, the surface was roughened by zirconia (mean part. dia. = 3  $\mu\text{m}$ ) sandblasting to  $R_{\text{max}} = 4 \mu\text{m}$  according to JIS B 0601.

- 10 Furthermore, with the silicon wafer being supported with the support pins at a distance of 100  $\mu\text{m}$  from the surface of the aluminum nitride ceramic board, heating under reduced pressure was performed up to 500 °C.

(Example 5)

- 15 The procedure of Example 1 was substantially repeated. However, the external terminals were attached using a Ni-Au brazing material, which was caused to reflow at 1030 °C. In addition, the surface was roughened by zirconia (mean part. dia. = 15  $\mu\text{m}$ ) sandblasting to  $R_{\text{max}} = 18 \mu\text{m}$  according to JIS B 0601.

- 20 Furthermore, with the wafer being supported with the support pins at a distance of 100  $\mu\text{m}$  from the surface of the aluminum nitride ceramic board, heating under reduced pressure was performed up to 500 °C.

25 (Example 6)

- The procedure of Example 1 was substantially repeated. However, the external terminals were attached using a Ni-Au brazing material, which was caused to reflow at 1030 °C. In addition, the surface was roughened by zirconia (means part. dia. = 200  $\mu\text{m}$ ) sandblasting to  $R_{\text{max}} = 180 \mu\text{m}$  according to JIS B 0601.

- 30 Furthermore, with the wafer being supported with the support pins at a distance of 100  $\mu\text{m}$  from the surface of the aluminum nitride ceramic board, heating under reduced pressure was performed up to 500 °C.

(Comparative Example 4)

The procedure of Example 1 was substantially repeated. However, the external terminals were attached using a Ni-Au brazing material, which was cause to reflow at 1030 °C. No sandblasting was carried out. Therefore, the surface roughness was  $R_{\max} = 0.1 \mu\text{m}$ .

Then, with the wafer being supported with the support pins at a distance of 100  $\mu\text{m}$  from the surface of the aluminum nitride board, heating under reduced pressure was performed up to 500 °C.

(Comparative Example 5)

The procedure of Example 1 was substantially repeated. However, the external terminals were attached using a Ni-Au brazing material, which was caused to reflow at 1030 °C. In addition, the surface was roughened by zirconia (means part. dia. = 250  $\mu\text{m}$ ) sandblasting to  $R_{\max} = 210 \mu\text{m}$  according to JIS B 0601.

Then, with the wafer being supported with the support pins at a distance of 100  $\mu\text{m}$  from the surface of the aluminum nitride board, heating under reduced pressure was performed up to 500 °C.

Using the ceramic heaters obtained above in Examples 1 to 6 and Comparative Examples 1 to 5, the Y, Na, Ca, Rb, Li and B contents were measured by fluorescent X-ray analysis and the thermal conductivity of each heater plate was measured by the laser flush method. In addition, a semiconductor wafer was set on each heater plate and heated to 500 °C and the diffusion of Y, Na, Ca, Rb, Li and B into the wafer was measured by fluorescent X-ray analysis. Moreover, the difference of temperature between the wafer surface and the wafer-heating surface was measured with the thermocouples.

The results are shown in Table 1.

Table 1

	Thermal conductivity (W/m*K)	Impurity content (%): B, Na in ppm						Diffusion of impurities into silicon wafer						Temp. difference (°C)
		Y	Na	Ca	Li	Rb	B	Y	Na	Ca	Li	Rb	B	
Ex. 1	180	1.3	0.1	-	-	-	0.8	No	No	-	-	-	No	0.5
Ex. 2	180	-	0.1	1.5	-	-	0.8	-	No	No	-	-	No	0.8
Ex. 3	180	-	0.1	-	0.8	0.6	0.8	-	No	-	No	No	No	0.8
Ex. 4	180	1.3	0.1	-	-	-	0.8	No	No	-	-	-	No	0.5
Ex. 5	180	1.3	0.1	-	-	-	0.8	No	No	-	-	-	No	0.6
Ex. 6	180	1.3	0.1	-	-	-	0.8	No	No	-	-	-	No	0.8
Compar. Ex. 1	180	1.3	0.1	-	-	-	0.8	Yes	Yes	-	-	-	Yes	0.5
Compar. Ex. 2	80	-	0.1<	-	-	-	0.1<	-	No	-	-	-	No	0.6
Compar. Ex. 3	180	1.3	0.1	-	-	-	0.8	No	No	-	-	-	No	2.0
Compar. Ex. 4	180	1.3	0.1	-	-	-	0.8	Yes	Yes	-	-	-	Yes	0.5
Compar. Ex. 5	180	1.3	0.1	-	-	-	0.8	Yes	Yes	-	-	-	Yes	2.0

It will be apparent from the data in Table 1 that whereas the thermal conductivity of the heater plate was high and no diffusion of Y and other impurities was found with the ceramic heaters according to Examples 1 to 3, there occurred a diffusion of Y and other impurities into the wafer because of flatness of the wafer-heating surface of the ceramic heater in Comparative Example 1 and the thermal conductivity of the heater plate of the ceramic heater was low in Comparative Example 2 in which Y had not been added. Furthermore, in Comparative Example 3, the wafer was not sufficiently heated.

In Examples 4 to 6 where the semiconductor wafer was not directly set on the heating surface but held in suspension by support pins and followed by heating, no diffusion of Y and other impurities was observed. In contrast, contamination of the wafer with Y, Na or B was observed in Comparative Examples 4 and 5. This contamination was suspected to have been caused by vaporization of Y and other impurities.

#### 20 (Example 7) Manufacture of an electrostatic chuck

(1) Using a paste prepared by mixing 100 weight parts of aluminum nitride powder (manufactured by Tokuyama Co.; average particle diameter:  $1.1 \mu\text{m}$ ) containing Na and B in the concentrations of 0.1 ppm and 0.8 ppm, respectively, 4 weight parts of yttria (average particle diameter:  $0.4 \mu\text{m}$ ), 11.5 weight parts of acrylic binder, 0.5 weight part of dispersant and 53 weight parts of alcohol comprising 1-butanol and ethanol, a 0.47 mm-thick green sheet was molded by the doctor blade method.

(2) This green sheet was dried at  $80^\circ\text{C}$  for 5 hours and formed with through holes to serve as the plated-through holes for electrical connection to external terminals by the punching method.

(3) A conductor paste A was prepared by mixing 100 weight parts of a tungsten carbide powder ( $1 \mu\text{m}$  in average particle size),

3.0 weight parts of acrylic binder, 3.5 weight parts of the solvent  $\alpha$ -terpineol, and 0.3 weight part of dispersant.

A conductor paste B was prepared by mixing 100 weight parts of a tungsten powder ( $3\text{ }\mu\text{m}$  in average particle size),  
 5 1.9 weight parts of acrylic binder, 3.7 weight parts of the solvent  $\alpha$ -terpineol, and 0.2 weight part of dispersant.

Using this conductive paste A, a conductor paste layer was formed on the green sheet by screen printing. The printing pattern was a concentric circular pattern. On another green  
 10 sheet, a conductor paste layer was formed according to the static electrode pattern illustrated in Fig. 4.

In addition, the through holes to serve as plated-through holes for connecting external terminals were filled with the conductor paste B. The static electrode pattern comprising  
 15 comb-shaped electrodes 30 (20b, 30b), with 20b, 30b being connected to 20a and 30a, respectively.

The green sheet 500 which had gone through the above processes was laminated with 34 and 13 units of green sheet 500' not printed with the tungsten paste on the top and bottom sides,  
 20 respectively, and the green sheet 500 printed with the conductor paste in the static electrode pattern was further laminated thereon. In addition, 2 units of the green sheet 500' not printed with the tungsten paste were further laminated. The laminating temperature and pressure were  $130\text{ }^{\circ}\text{C}$  and  $80\text{ kg/cm}^2$   
 25 (Fig. 5 (a)).

(4) The above laminate was degreased in a nitrogen gas atmosphere at  $600\text{ }^{\circ}\text{C}$  for 5 hours and hot-pressed at  $1890\text{ }^{\circ}\text{C}$  and  $150\text{ kg/cm}^2$  for 3 hours to provide a 3 mm-thick aluminum nitride plate. From this plate, a disk 230 mm in diameter was cut out  
 30 to provide an aluminum nitride disk internally provided with a  $6\text{ }\mu\text{m}$ -thick  $\times 10\text{ mm}$ -wide resistance heating element 50 and a chuck positive electrode static layer 20 and a chuck negative electrode static layer 30, both having a thickness of  $6\text{ }\mu\text{m}$  (Fig. 5 (b)).

35 (5) The disk obtained in (4) was followed by abrasion with a

diamond wheel, and with a mask set in position, its surface layer was formed with blind holes (diameter: 1.2 mm, depth: 2.0 mm) for accepting thermocouples by zirconia (mean part. dia. 1  $\mu$  m) sandblasting.

5           The surface roughness after sandblasting was  $R_{max} = 2$   $\mu$  m.

(6) Then, the plated-through holes were bored to form cavities 130, 140 (Fig. 5 (c)) and external terminals 60, 180 comprising Koval® were connected and fixed in the cavities 130, 140 with  
10 a Ni-Au gold brazing material heated for reflow at 700 °C (Fig. 5 (d)).

The connection of the external terminals is preferably a 3-point tungsten support system which is high in connection reliability.

15 (7) Then, a plurality of thermocouples for temperature control were embedded in the blind holes to complete the manufacture of an electrostatic chuck having a resistance heating element as illustrated in Fig. 3.

A silicon wafer was set in position, and with a 1 kV  
20 electrode being applied for attraction of the wafer, the wafer was heated at 300 °C.

In Example 7, a heater equipped with an electrostatic chuck was manufactured by providing static electrodes. Contamination of the wafer with Y and other impurities was not  
25 observed.

(Example 8) Manufacture of a SiC ceramic heater

(1) A composition comprising 100 weight parts of SiC powder (mean particle diameter: 0.3  $\mu$  m), 0.5 weight part of the  
30 sintered aid  $B_4C$ , 12 weight parts of acrylic binder and the balance of alcohol was spray-dried to prepare a granulation.

(2) This granulation was placed in a metal mold and molded into a flat plate form to give a green plate.

(3) This green plate was hot-pressed at 2100 °C and 180 kg/cm<sup>2</sup>  
35 to prepare a 3 mm-thick SiC plate.



From the surface of this plate, a disk having a diameter of 210 mm was cut out for use as a ceramic substrate. The surface of this ceramic substrate was polished with a diamond paste having a grit diameter of  $0.05\ \mu\text{m}$  (Maruto) to prepare a heating surface with a roughness of  $R_{\text{max}} = 0.08\ \mu\text{m}$ .

This SiC ceramic substrate was coated with a glass paste (Showei Kagaku Kogyo; G-5232N) and sintered at  $1000\ ^\circ\text{C}$  for 1 hour to form a  $2\ \mu\text{m}$ -thick  $\text{SiO}_2$  film on the surface.

This substrate was drilled to form a through-hole 15 for accepting a silicon wafer lifter pin and blind-holes 14 for embedding thermocouples (diameter:  $1.1\ \text{mm}$   $\times$  depth :  $2\ \text{mm}$ ).

(4) The ceramic substrate obtained in the above Step (3) was printed with a conductor paste by the screen printing technique. The recipe of the conductor paste used was: 90 weight parts of silver scales (Showei Kagaku Kogyo; Ag-540), 10 weight parts of platinum needles (Showei Kagaku Kogyo; Pt-401), 7.5 weight parts of silica, 1.5 weight parts of boron oxide, 6 weight parts of zinc oxide, and 30 weight parts of the organic vehicle cellulose acetate.

(5) The ceramic substrate printed with the conductor paste in the above manner was then heated at  $780\ ^\circ\text{C}$  to sinter the silver and platinum in the conductor paste and bake them onto the ceramic substrate to provide a heating element. This heating element was  $5\ \mu\text{m}$  thick  $\times$   $10\ \text{mm}$  wide and had an area resistivity of  $0.13\ \Omega/\square$ .

(6) The surface of the heating element was coated with a glass paste (Showei Kagaku Kogyo; G-5177) and heated at  $1000\ ^\circ\text{C}$  to form a cover layer.

(7) To the parts of this heating element to which terminals for electrical connection to a power supply were to be attached, a silver brazing material was screen-printed to form a solder layer.

Then, external terminals 13 of kovar were set on the solder layer and the solder was caused to reflow by heating at  $780\ ^\circ\text{C}$ , whereby the external terminals 13 were rigidly secured

to the surface of the heating element 12. The semiconductor supporting pins were also inserted and secured in position.

(8) Thermocouples for temperature control were fitted into the blind holes 14 and secured rigidly with a ceramic adhesive (Toa Gosei; Aron Ceramic) to complete a ceramic heater 10.

(Example 9)

The procedure of Example 8 was repeated except that, after the polishing with a diamond paste having a grit diameter of 0.05  $\mu\text{m}$  (Maruto), the substrate surface was subjected to sand blasting with an SiC powder having a mean particle diameter of 5  $\mu\text{m}$  to form a rough surface of  $R_{\text{max}} = 6 \mu\text{m}$ .

(Example 10)

The procedure of Example 8 was repeated except that, after the polishing with a diamond paste having a grit diameter of 0.05  $\mu\text{m}$  (Maruto), the substrate surface was subjected to sand blasting with an SiC powder having a mean particle diameter of 180  $\mu\text{m}$  to form a rough surface of  $R_{\text{max}} = 200 \mu\text{m}$ .

(Comparative Example 6)

The procedure of Example 8 was repeated except that the substrate surface was polished with a diamond paste having a grit diameter of 0.01  $\mu\text{m}$  (Maruto) to form a rough surface of  $R_{\text{max}} = 0.03 \mu\text{m}$ .

(Comparative Example 7)

The procedure of Example 8 was repeated except that, after the polishing with a diamond paste having a grit size of 0.05  $\mu\text{m}$  (Maruto), the substrate surface was subjected to sand blasting with an SiC powder having a mean particle diameter of 250  $\mu\text{m}$  to form a rough surface of  $R_{\text{max}} = 210 \mu\text{m}$ .

(Example 11)

Manufacture of an aluminum nitride ceramic heater

(1) A composition comprising 100 weight parts of aluminum nitride powder (mean particle diameter:  $0.6\ \mu\text{m}$ ), 4 weight parts of yttria (mean particle diameter:  $0.4\ \mu\text{m}$ ), 12 weight parts of acrylic binder and the balance of alcohol was spray-dried to prepare a granulation.

(2) This granulation was placed in a metal mold and molded in a flat sheet form to give a green plate.

(3) This green plate was hot-pressed at  $1800\ ^\circ\text{C}$  and  $200\ \text{kg}/\text{cm}^2$  to prepare a 3 mm-thick aluminum nitride plate.

From the surface of this plate, a disk having a diameter of 210 mm was cut out for use as a ceramic substrate 11. The same glass paste as used in Example 8 was coated on the surface of the above substrate and dried and sintered to form a  $2\ \mu\text{m}$ -thick  $\text{SiO}_2$  film. Then, the surface of this ceramic substrate was polished with a diamond paste having a grit diameter of  $0.05\ \mu\text{m}$  (Maruto) to prepare a heating surface with a roughness of  $R_{\text{max}} = 0.08\ \mu\text{m}$ .

This substrate was drilled to form a through-hole 35 for accepting a silicon wafer lifter pin and blind-holes 14 for embedding thermocouples (diameter:  $1.1\ \text{mm}$   $\times$  depth :  $2\ \text{mm}$ ).

(4) A conductor paste was screen-printed on the ceramic substrate 11 obtained in the above step (3). The recipe of the conductor paste used was:

50 weight parts of silver flakes (Showei Kagaku Kogyo; Ag-540), 50 weight parts of palladium spheres (Showei Kagaku Kogyo; Pd-225), 10 weight parts of zinc oxide, 8 weight parts of silica, 2 weight parts of boron oxide, and 30 weight parts of the organic vehicle cellulose acetate.

(5) The ceramic substrate 11 printed with the conductor paste in the above manner was then heated at  $780\ ^\circ\text{C}$  to sinter the silver and Pd in the conductor paste and bake them onto the ceramic substrate 11 to provide a heating element 12. This silver-Pd heating element 12 was  $5\ \mu\text{m}$  thick  $\times$   $15\ \text{mm}$  wide and had an area resistivity of  $5.09\ \Omega/\square$ .

(6) The surface of the heating element 12 was coated with a glass

paste (Showei Kagaku Kogyo; G-5177) and heated at 1000 °C to form a cover layer.

(7) To the parts of this heating element to which terminals for electrical connection to a power supply were to be attached, a silver-lead solder paste was screen-printed to form a solder layer.

Then, external terminals 13 of kovar were set on the solder layer and the solder was caused to reflow by heating at 780 °C, whereby the external terminals 13 were rigidly secured to the surface of the heating element 12. The semiconductor supporting pins were also inserted and secured in position. (8) Thermocouples for temperature control were fitted into the blind holes 14 and secured rigidly with a ceramic adhesive (Toa Gosei; Aron Ceramic) to complete a ceramic heater.

(Example 12)

The procedure of Example 11 was repeated except that, after the polishing with a diamond paste having a grit diameter of 0.05  $\mu\text{m}$  (Maruto), the substrate surface was subjected to sand blasting with an SiC powder having a mean particle diameter of 5  $\mu\text{m}$  to form a rough surface of  $R_{\text{max}} = 6 \mu\text{m}$ .

(Example 13)

The procedure of Example 11 was repeated except that, after the polishing with a diamond paste having a grit diameter of 0.05  $\mu\text{m}$  (Maruto), the substrate surface was subjected to sand blasting with an SiC powder having a mean particle diameter of 180  $\mu\text{m}$  to form a rough surface of  $R_{\text{max}} = 200 \mu\text{m}$ .

(Comparative Example 8)

The procedure of Example 11 was repeated except that the surface of the substrate was polished with a diamond paste having a mean grit size of 0.01  $\mu\text{m}$  (Maruto) to form a rough surface of  $R_{\text{max}} = 0.03 \mu\text{m}$ .

(Comparative Example 9)

The procedure of Example 11 was repeated except that, after the surface polishing with a diamond paste having a mean grit size of  $0.05\ \mu\text{m}$ , the substrate was subjected to sand blasting with zirconia having a mean grit size of  $250\ \mu\text{m}$  to form a rough surface of  $R_{\text{max}} = 210\ \mu\text{m}$ .

In each of Examples 8 to 13 and Comparative Examples 6 to 9, a silicon wafer was heated to  $400\ ^\circ\text{C}$ , with the distance between the heating surface of the ceramic heater and the silicon wafer being maintained at  $100\ \mu\text{m}$  and the difference  $\Delta T$  between the highest and lowest temperatures of the silicon wafer was determined with Thermoviewer (Japan Datum; IR-16-2012-0012).

The results are shown below in Table 2.

15

Table 2

	Surface roughness $R_{\text{max}}\ (\mu\text{m})$	Difference between highest and lowest temperatures $\Delta T\ (^{\circ}\text{C})$
Example 8	0.08	4
Example 9	6	3
Example 10	180	4
Example 11	0.08	5
Example 12	6	4
Example 13	180	5
Compar. Ex. 6	0.03	8
Compar. Ex. 7	210	8
Compar. Ex. 8	0.03	10
Compar. Ex. 9	180	10

It is apparent from Table 2 above that the temperature difference is large when the surface roughness  $R_{\text{max}}$  is greater than  $200\ \mu\text{m}$  or smaller than  $0.05\ \mu\text{m}$ . This is probably because the temperature distribution of the heating surface is reflected in the silicon wafer when the surface roughness is too large or too small.

Incidentally, since SiC has a relatively higher heat conductivity ( $270\ \text{W/m}\cdot\text{K}$ ), the temperature difference of the

heating surface is smaller.

#### INDUSTRIAL APPLICABILITY

5 In the ceramic heater of the present invention, the constitution of which has been described above, the heater element contains at least one element selected from among Y and others and, therefore, has a high thermal conductivity so that the surface temperature of the heater plate promptly follows the temperature change of the heating element to control the  
10 temperature of the wafer-heating surface with high efficiency.

Moreover, because the wafer-heating surface has been roughened, the mode of its contact with a semiconductor wafer is point contact so that the thermal diffusion of impurity from the ceramic heater into the semiconductor wafer can be  
15 successfully prevented and contamination of the semiconductor wafer etc. due to vaporization of the impurity is also prevented.

This heater can be applied to an electrostatic chuck and a wafer prober.  
20

## CLAIMS

1. A ceramic heater comprising a ceramic substrate and a heating element disposed either on the surface or internally  
5 of the substrate,

wherein the work-heating surface has a JIS B 0601 surface roughness of  $R_{max} = 0.05$  to  $200 \mu m$ .

2. A ceramic heater comprising a ceramic substrate and  
10 a heating element disposed either on the surface or internally of the substrate,

wherein said ceramic substrate contains an element other than its dominant constituent elements and the work-heating surface of the heater has a JIS B 0601 surface roughness of  $R_{max}$   
15  $= 0.2$  to  $200 \mu m$ .

3. The ceramic heater according to Claim 1 or 2

wherein said ceramic substrate is at least one member selected from among a nitride ceramic, a carbide ceramic and  
20 an oxide ceramic.

4. A ceramic heater comprising a nitride ceramic substrate and a heating element either on the surface or internally of said substrate,

25 wherein said nitride ceramic substrate contains an element other than its principal constituent elements and the work-heating surface of the heater has a JIS B 0601 surface roughness of  $R_{max} = 0.2$  to  $200 \mu m$ .

30 5. A ceramic heater comprising a nitride ceramic substrate and a heating element either on the surface or internally of said substrate

wherein said nitride ceramic board contains at least one element selected from Na, B, Y, Li, Rb and Ca and a work-heating  
35 surface has a JIS B 0601 roughness value of  $R_{max} = 0.2$  to  $200$

$\mu$  m.

6. The ceramic heater according to Claim 4 or 5  
wherein said nitride ceramic board has the form of a disk  
5 having a diameter of more than 150 mm.

7. The ceramic heater according to Claim 4, 5 or 6  
wherein the content of at least one element selected from  
the group consisting of Y, Li, Rb and Ca is not less than 0.1  
10 weight %.

8. The ceramic heater according to Claim 4, 5 or 6  
wherein the content of at least one element selected from  
the group consisting of Na and B is not less than 0.05 ppm.  
15



## ABSTRACT

The present invention has its object to provide a ceramic heater which has a high thermal conductivity so that the surface  
5 temperature of the heater plate promptly follows the temperature change of the heating element to control the temperature of the wafer-heating surface with high efficiency and the thermal diffusion of impurity from the ceramic heater can be successfully prevented.

10 This invention provides a ceramic heater comprising a nitride ceramic substrate and a heating element either on the surface or internally of said substrate

wherein said nitride ceramic board contains an element other than constituent elements of nitride ceramics and a  
15 work-heating surface has the JIS B 0601 surface roughness of  $R_{max} = 0.2$  to  $200 \mu m$ .

Fig. 1

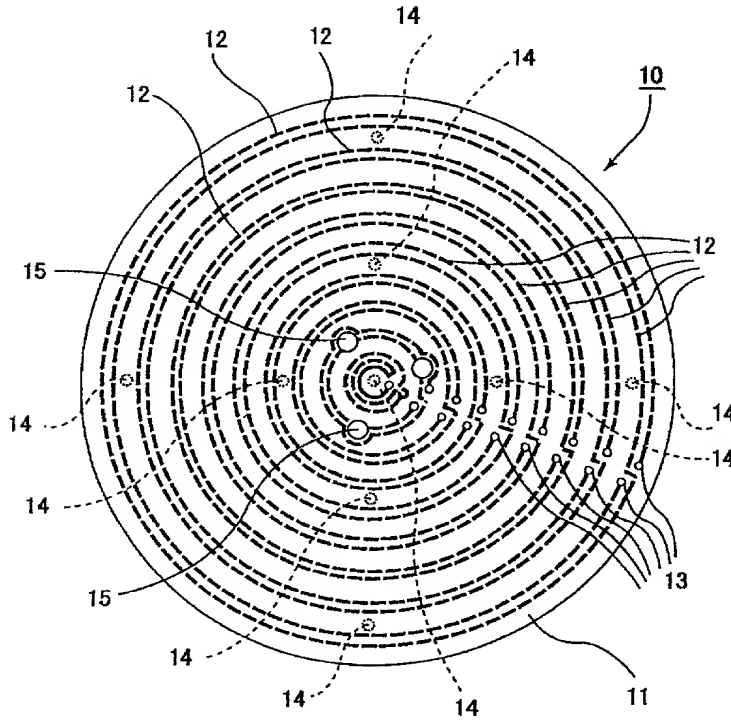
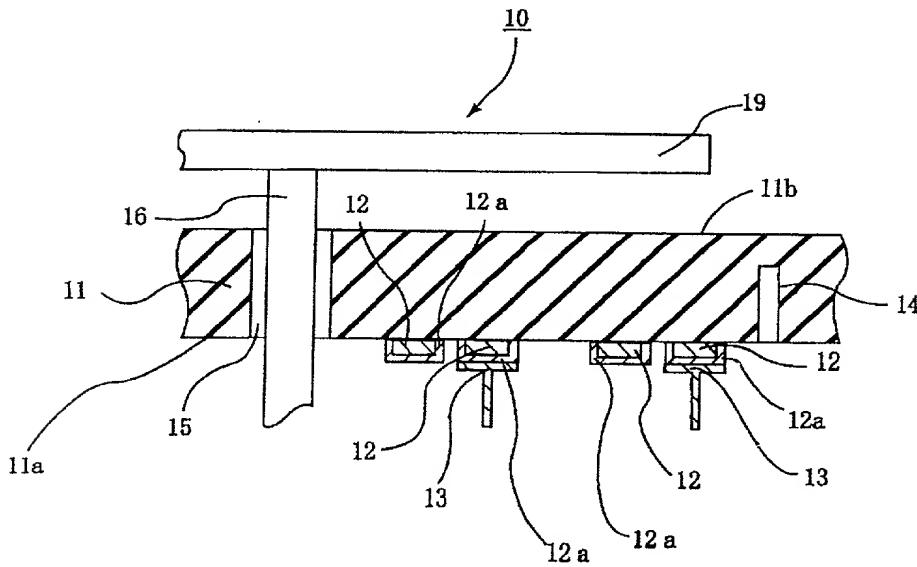


Fig. 2



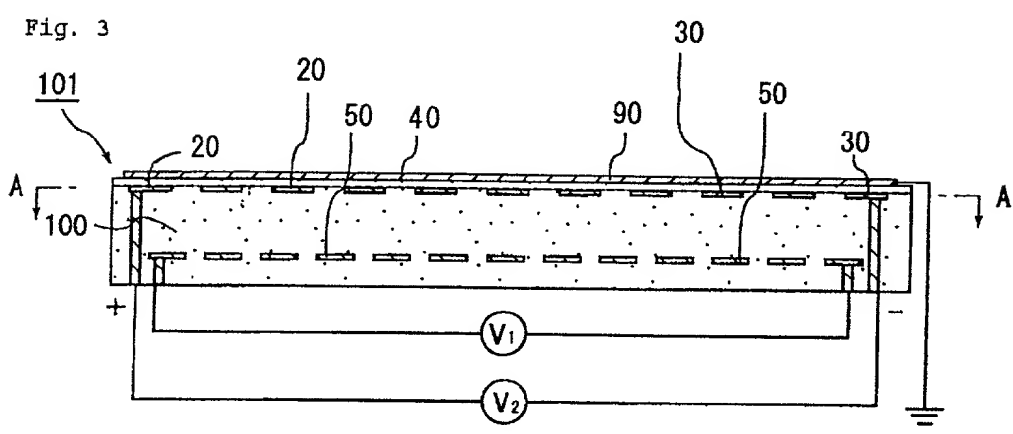


Fig. 4

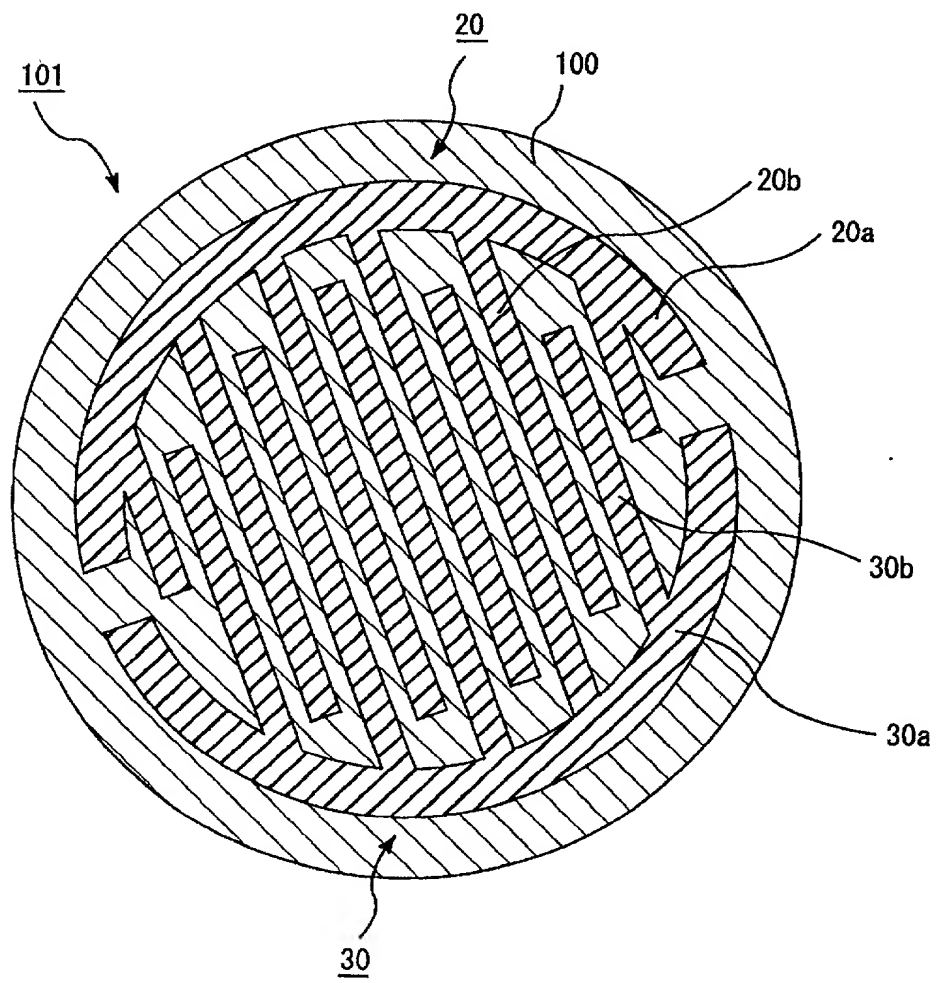


Fig. 5

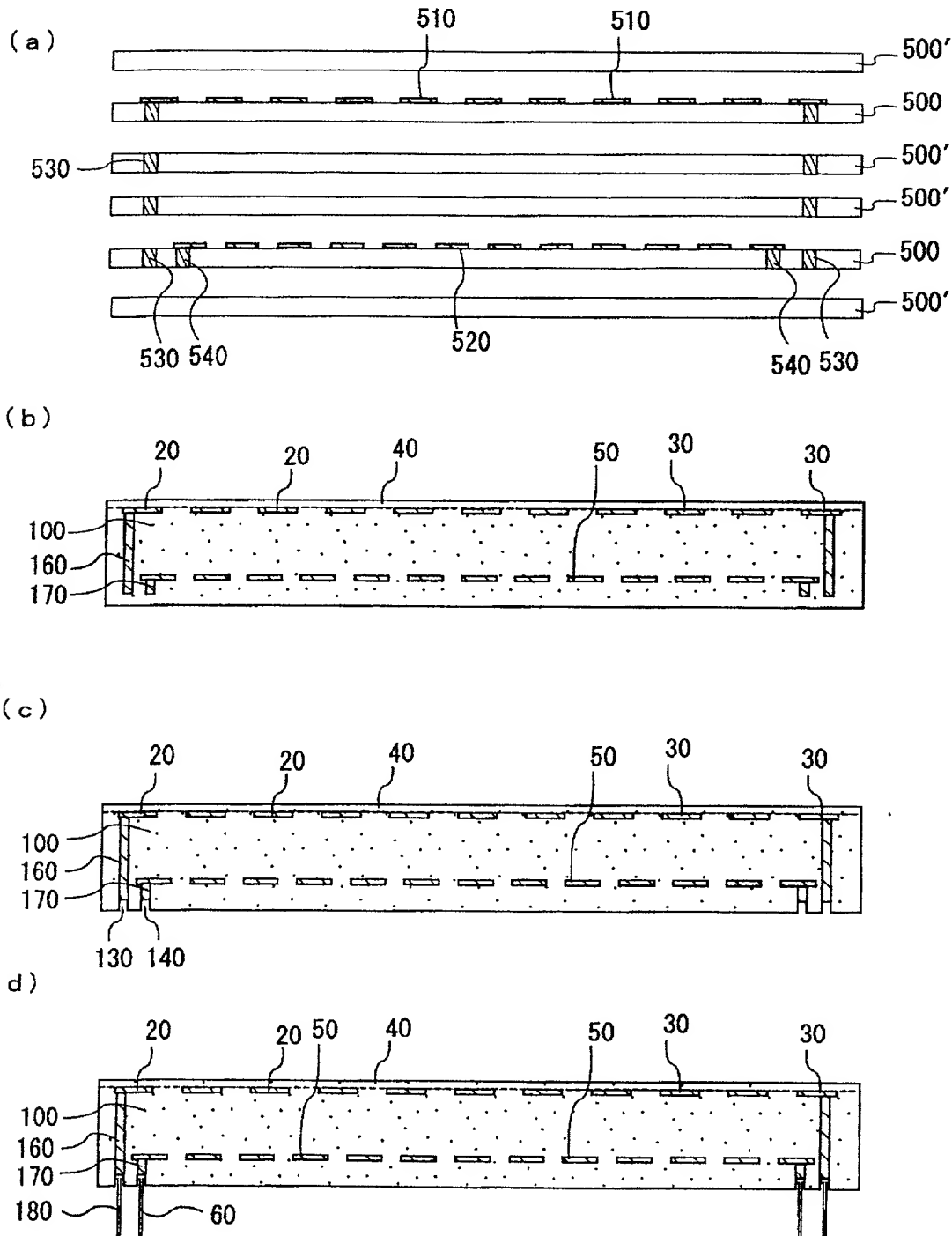
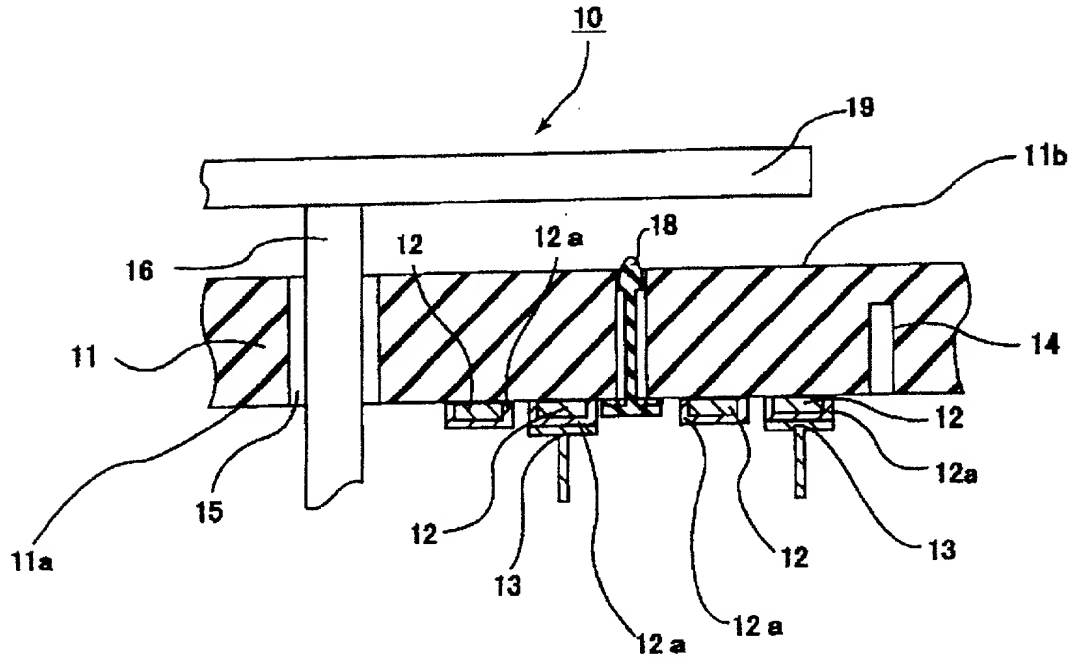


Fig. 6



# Declaration and Power of Attorney For Patent Application

205235US0PCT

## 特許出願宣言書及び委任状

### Japanese Language Declaration

#### 日本語宣言書

下記の氏名の発明者として、私は以下の通り宣言します。

As a below named inventor, I hereby declare that:

私の住所、私書箱、国籍は下記の私の氏名の後に記載された通りです。

My residence, post office address and citizenship are as stated next to my name.

下記の名称の発明に関して請求範囲に記載され、特許出願している発明内容について、私が最初かつ唯一の発明者（下記の氏名が一つの場合）もしくは最初かつ共同発明者（下記の名称が複数の場合）であると信じています。

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled.

CERAMIC HEATER

上記発明の明細書は、

本書に添付されています。

\_\_\_\_月\_\_\_\_日に提出され、米国出願番号または特許協定条約国際出願番号を\_\_\_\_とし、  
(該当する場合) \_\_\_\_\_に訂正されました。

the specification of which

☐ is attached hereto.

☒ was filed on 2 May 2000

as United States Application Number or

PCT International Application Number

PCT/JP00/02909

and was amended on

\_\_\_\_ (if applicable).

私は、特許請求範囲を含む上記訂正後の明細書を検討し、内容を理解していることをここに表明します。

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

私は、連邦規則法典第37編第1条56項に定義されるとおり、特許資格の有無について重要な情報を開示する義務があることを認めます。

I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations, Section 1.56.

**Japanese Language Declaration**  
(日本語宣言書)

私は、米国法典第35編119条 (a) - (d) 項又は365条 (b) 項に基づき下記の、米国以外の国の少なくとも一カ国を指定している特許協力条約365 (a) 項に基づく国際出願、又は外国での特許出願もしくは発明者証の出願についての外国優先権をここに主張するとともに、優先権を主張している、本出願の前に出願された特許または発明者証の外国出願を以下に、枠内をマークすることで、示しています。

Prior Foreign Application(s)

外国での先行出願

11/225695

JAPAN

(Number)  
(番号)

(Country)  
(国名)

PCI/JP00/00881

JAPAN

(Number)  
(番号)

(Country)  
(国名)

I hereby claim foreign priority under Title 35, United States Code, Section 119 (a)-(d) or 365(b) of any foreign application(s) for patent or inventor's certificate, or Section 365(a) of any PCT International application which designated at least one country other than the United States, listed below and have also identified below, by checking the box, any foreign application for patent or inventor's certificate, or PCT International application having a filing date before that of the application on which priority is claimed.

Priority Claimed

優先権主張

9 AUGUST 1999

(Day/Month/Year Filed)  
(出願年月日)

17 FEBRUARY 2000

(Day/Month/Year Filed)  
(出願年月日)

☒

Yes  
はい

☒

Yes  
はい

☐

No  
いいえ

☐

No  
いいえ

私は、第35編米国法典119条 (e) 項に基づいて下記の米国特許出願規定に記載された権利をここに主張いたします。

(Application No.)  
(出願番号)

(Filing Date)  
(出願日)

私は、下記の米国法典第35編120条に基づいて下記の米国特許出願に記載された権利、又は米国を指定している特許協力条約365条 (c) に基づく権利をここに主張します。また、本出願の各請求範囲の内容が米国法典第35編112条第1項又は特許協力条約で規定された方法で先行する米国特許出願に開示されていない限り、その先行米国出願書提出日以降で本出願書の日本国内または特許協力条約国際提出日までの期間中に入手された、連邦規則法典第37編1条56項で定義された特許資格の有無に関する重要な情報について開示義務があることを認識しています。

PCI/JP00/02909

2 MAY 2000

(Application No.)  
(出願番号)

(Filing Date)  
(出願日)

(Application No.)  
(出願番号)

(Filing Date)  
(出願日)

私は、私自信の知識に基づいて本宣言書で私が行なう表明が真実であり、かつ私の入手した情報と私の信じているところに基づく表明が全て真実であると信じていること、さらに故意になされた虚偽の表明及びそれと同等の行為は米国法典第18編第1001条に基づき、罰金または拘禁、もしくはその両方により処罰されること、そしてそのような故意による虚偽の声明を行えば、出願した、又は既に許可された特許の有効性が失われることを認識し、よってここに上記のごとく宣誓を致します。

I hereby claim the benefit under Title 35, United States Code, Section 119(e) of any United States provisional application(s) listed below.

(Application No.)  
(出願番号)

(Filing Date)  
(出願日)

I hereby claim the benefit under Title 35, United States Code, Section 120 of any United States application(s), or Section 365(c) of any PCT International application designating the United States, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States or PCT International application in the manner provided by the first paragraph of Title 35, United States Code Section 112, I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations, Section 1.56 which became available between the filing date of the prior application and the national or PCT International filing date of application.

(Status: Patented, Pending, Abandoned)  
(現況: 特許許可済、係属中、放棄済)

(Status: Patented, Pending, Abandoned)  
(現況: 特許許可済、係属中、放棄済)

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Japanese Language Declaration  
(日本語宣言書)

委任状：私は下記の発明者として、本出願に関する一切の手続きを米特許商標局に対して遂行する弁理士または代理人として、下記の者を指名いたします。  
(弁護士、または代理人の指名及び登録番号を明記のこと)

POWER OF ATTORNEY: As a named inventor, I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and transact all business in the Patent and Trademark Office connected therewith: (list name and registration number)

Norman F. Oblon, Reg. No. 24,618; Marvin J. Spivak, Reg. No. 24,913; C. Irvin McClelland, Reg. No. 21,124; Gregory J. Maier, Reg. No. 25,599; Arthur I. Neustadt, Reg. No. 24,854; Richard D. Kelly, Reg. No. 27,757; James D. Hamilton, Reg. No. 28,421; Eckhard H. Kuesters, Reg. No. 28,870; Robert T. Pous, Reg. No. 29,099; Charles L. Gholz, Reg. No. 26,395; William E. Beaumont, Reg. No. 30,996; Jean-Paul Lavalleye, Reg. No. 31,451; Stephen G. Baxter, Reg. No. 32,884; Richard L. Treanor, Reg. No. 36,379; Steven P. Weihrauch, Reg. No. 32,829; John T. Goolkasian, Reg. No. 26,142; Richard L. Chinn, Reg. No. 34,305; Steven E. Lipman, Reg. No. 30,011; Carl E. Schlier, Reg. No. 34,426; James J. Kulbaski, Reg. No. 34,648; Richard A. Neifeld, Reg. No. 35,299; J. Derek Mason, Reg. No. 35,270; Surinder Sachar, Reg. No. 34,423; Jeffrey B. McIntyre, Reg. No. 36,867; William T. Enos, Reg. No. 33,128; Michael E. McCabe, Jr., Reg. No. 37,182; Bradley D. Lytle, Reg. No. 40,073; and Michael R. Casey, Reg. No. 40,294, with full powers of substitution and revocation.

書類送付先

Send Correspondence to:

OBLON, SPIVAK, MCCLELLAND, MAIER & NEUSTADT, P.C.  
FOURTH FLOOR  
1755 JEFFERSON DAVIS HIGHWAY  
ARLINGTON, VIRGINIA 22202 U.S.A.

直接電話連絡先：(名前及び電話番号)

Direct Telephone Calls to: (name and telephone number)  
(703) 413-3000

単独発明者または第一の共同発明者の氏名	Full name of sole or first joint inventor
発明者の署名	Inventor's signature
日付	Date
住所	Residence
国籍	Citizenship
郵便の宛先	Post Office Address
第二の共同発明者の氏名	Full name of second joint inventor, if any
第二の共同発明者の署名	Second joint Inventor's signature
日付	Date
住所	Residence
国籍	Citizenship
郵便の宛先	Post Office Address

(第三以降の共同発明者についても同様に記載し、署名すること)

(Supply similar information and signature for third and subsequent joint inventors.)